

TIME		Monday, 7 May 2001		
7:00am-5:00pm	Registration			
8:30am-12:00pm	Tutorial I Life Cycle Assessment and Design for Environment: Useful Electronics Applications	Tutorial II Transition to Lead-Free Solder in Electronics	Tutorial III International and Domestic Regulatory Product Requirements for the Electronic Sector	
9:45am-10:00am	Morning Break			
12:00pm-1:00pm	Lunch for Day 1 Speakers			
1:15pm-2:15pm	Introduction by Co-Chairs PLENARY SPEAKER: Wayne S. Balta, Director, Corporate Environmental Affairs, IBM Corporation			
2:15pm-2:30pm	Afternoon Break			
2:30pm-4:30pm	Session 1 DFE Methodology & Tools I	Session 2 Manufacturing Processes I	Session 3 Corporate Strategies for Environmental Performance	
4:45pm-5:45pm	Roundtable Discussions and Reception			
5:45pm-7:45pm	Exhibits			
TIME		Tuesday, 8 May 2001		
7:00am-5:00pm	Registration			
7:30am-8:30am	Breakfast for Day 2 Speakers			
8:30am-9:45am	Introduction by Co-Chairs KEYNOTE ADDRESS: Dr. Jonathan G. Koomey, Staff Scientist, Lawrence Berkeley National Laboratory			
9:45am-10:00am	Morning Break			
10:00am-12:00pm	Session 4 DFE Assessment Methods	Session 5 End of Life Strategies	Session 6 Life Cycle Impact Assessment	
12:00pm-1:45pm	Introduction by Co-Chairs LUNCHEON ADDRESS: Dr. Robert C. Pfahl, Jr., Director of Int'l & Environmental R & D, Motorola Advanced Technology Center			
2:00pm-3:30pm	Introduction by Co-Chairs PANEL: Environmental Implications of E-commerce			
3:30pm-3:45pm	Afternoon Break			
3:45pm-5:15pm	Session 7 DFE Methodology & Tools II	Session 8 Advanced Electronic Technologies & Modeling	Session 9 Analyses of the Impacts of E-everything	
5:30pm-7:30pm	Exhibit, Student Poster Session, Cocktail Reception			
TIME		Wednesday, 9 May 2001		
7:00am-12:00pm	Registration			
7:00am-8:00am	Breakfast for Day 3 Speakers			
8:00-9:30am	Session 10 DFE in Practice	Session 11 Advancing Electronic Technologies & Modeling	Session 12 Closed Loop Supply Chain Management	
9:30am-9:45am	Morning Break			
9:45am-11:45am	Session 13 DFE Metrics & Implementation	Session 14 Product Reuse & Recycling	Session 15 Environmental Analysis of Electronic Materials	